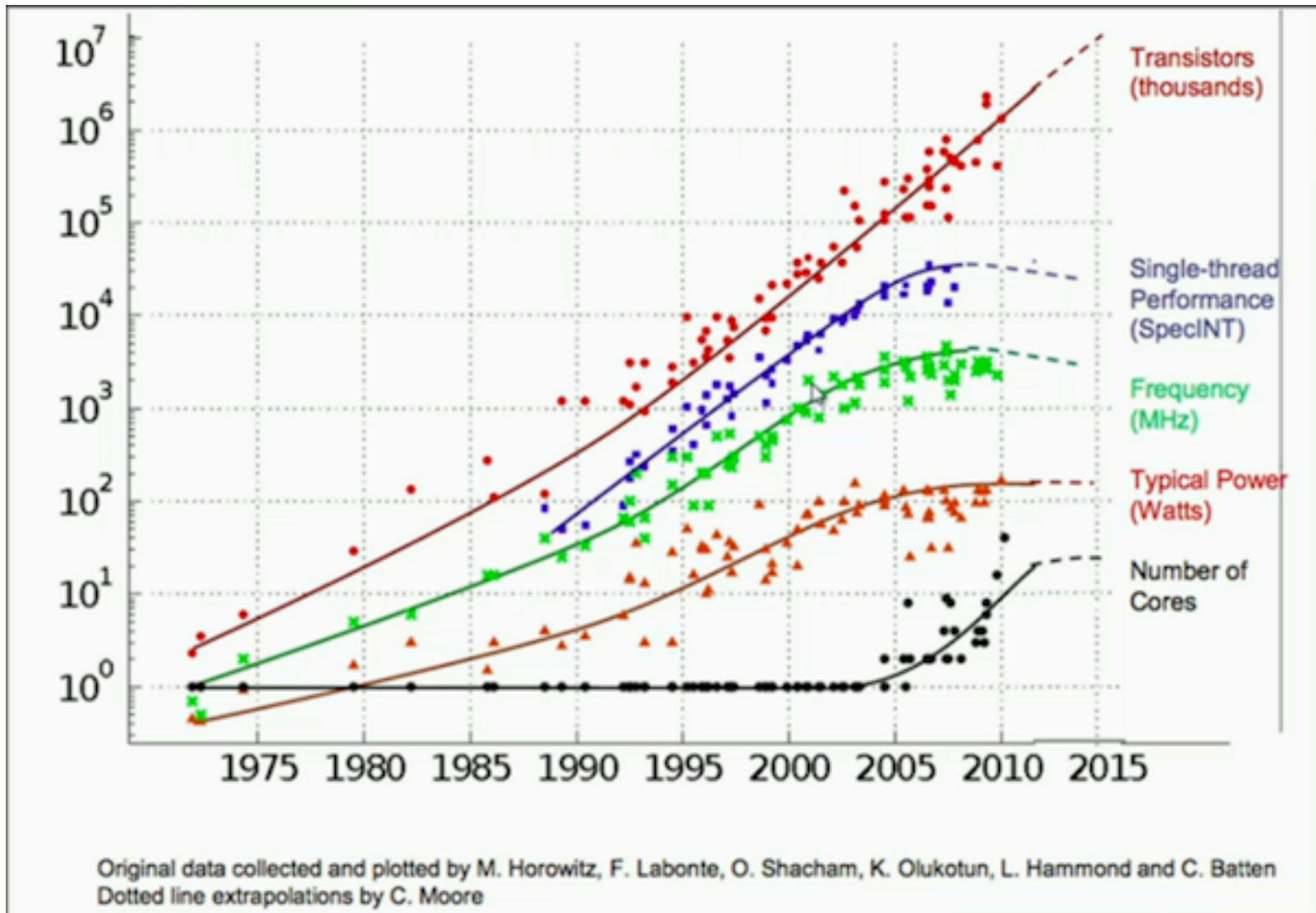


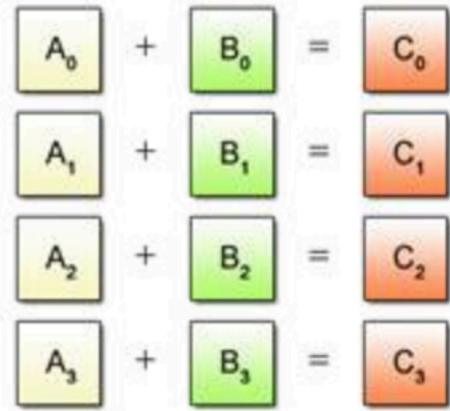
Architetture parallele

# Legge di Moore



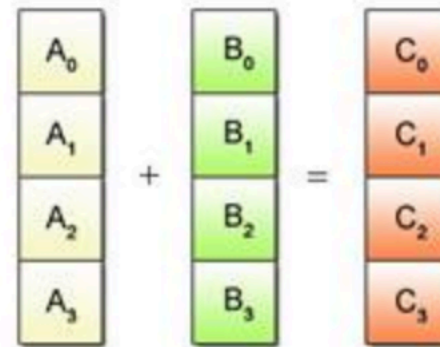
# Parallelismo in CPU single-core

Operazioni  
scalari



`addl %eax, %ebx`  
`addl %ecx, %edx`  
...

`padd %xmm0, %xmm1`

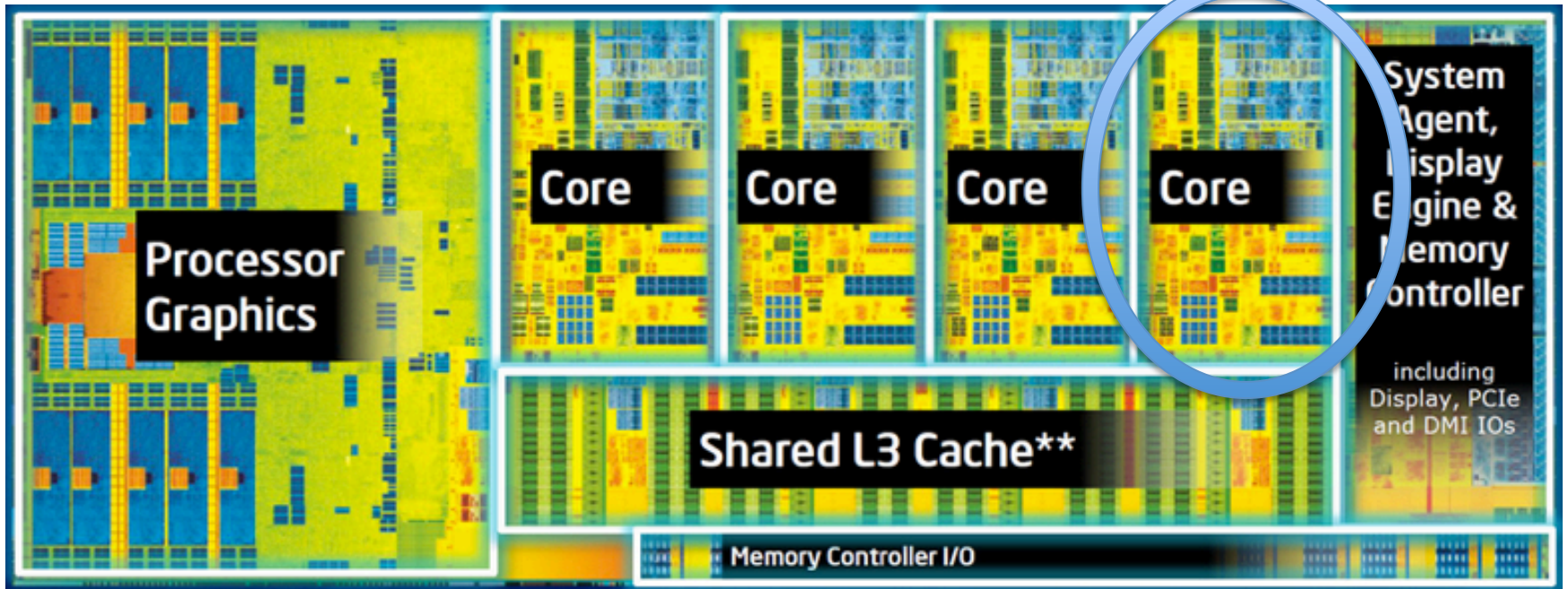
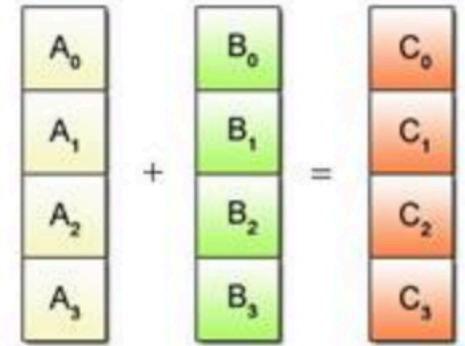


Operazioni  
vettorizzate  
(SSE)



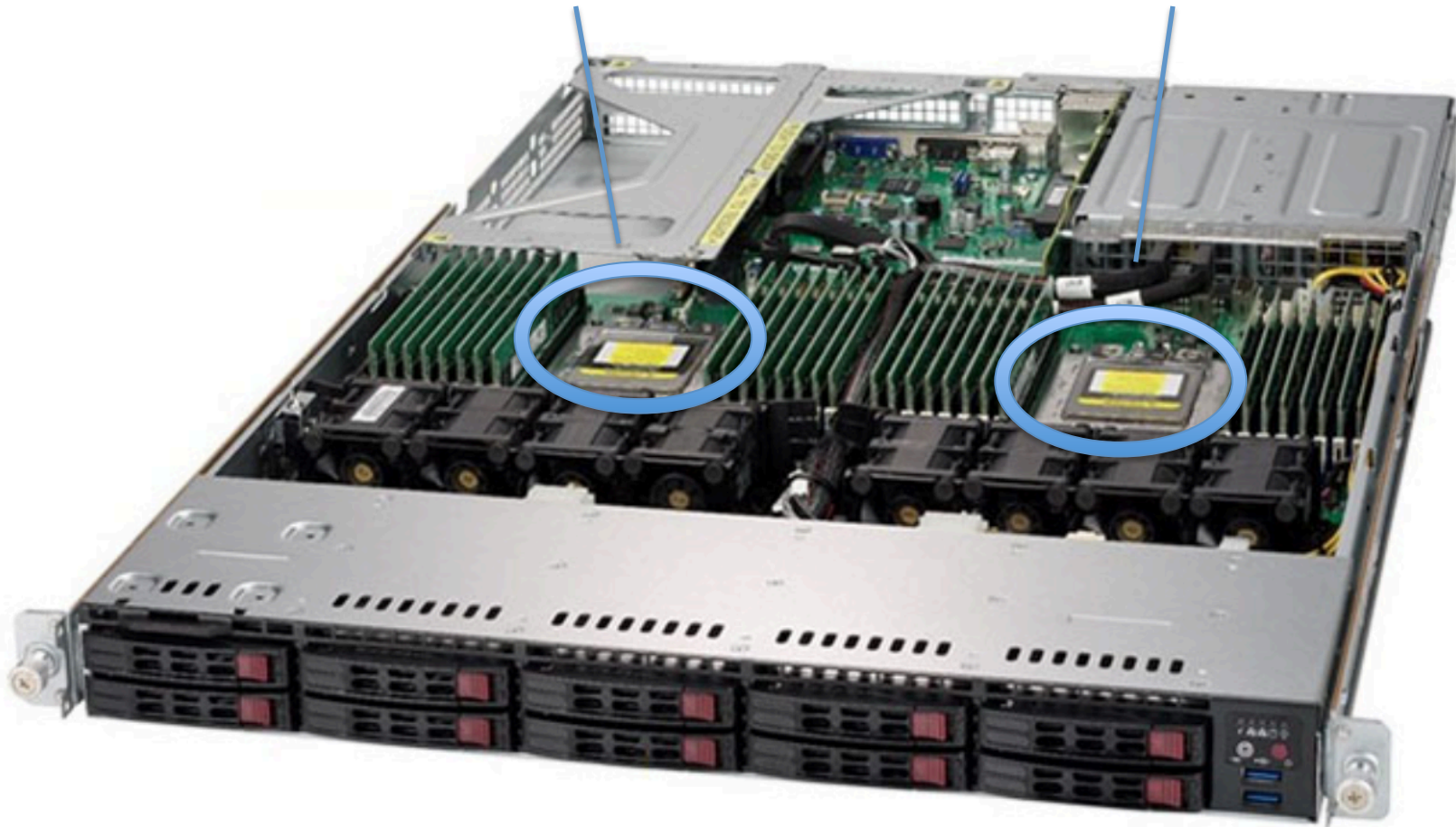
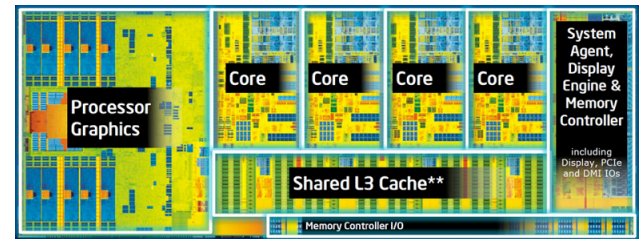
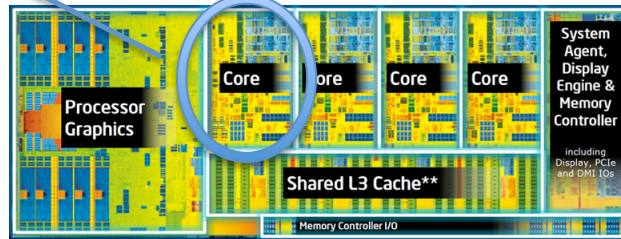
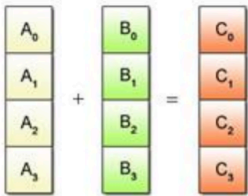
Intel  
Pentium 4  
2000-2008

# CPU multi-core

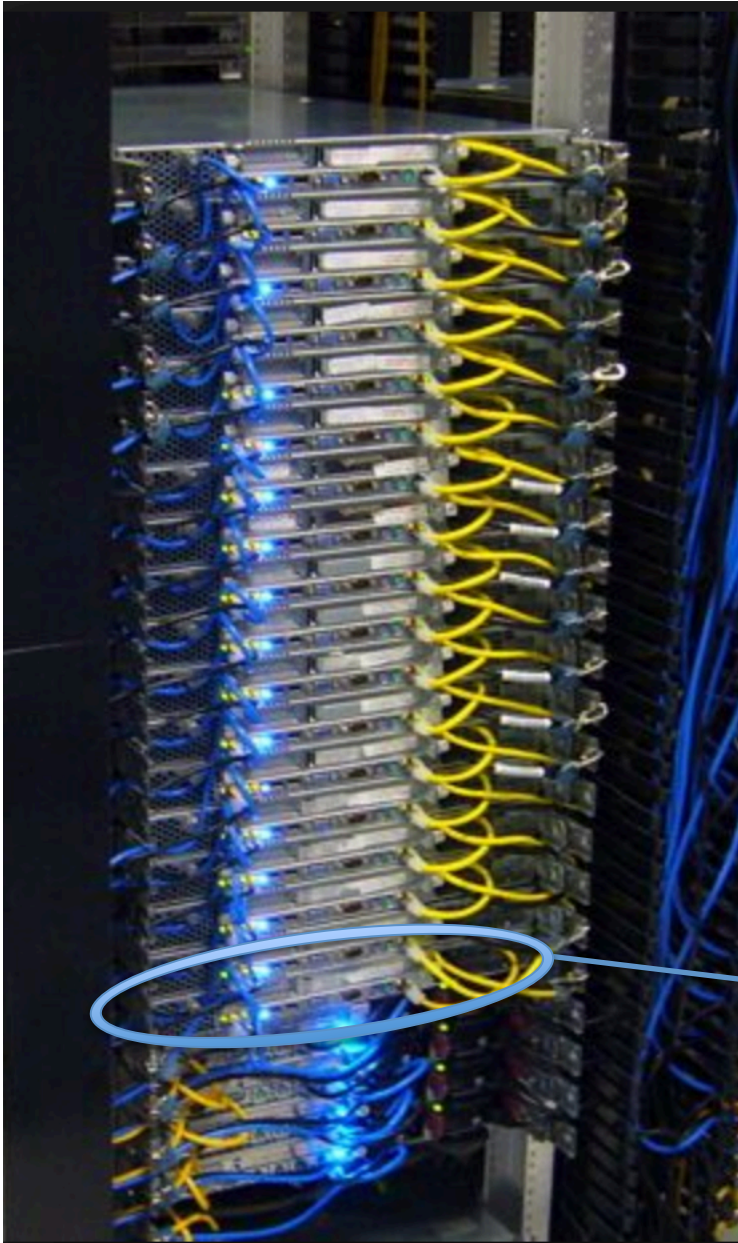
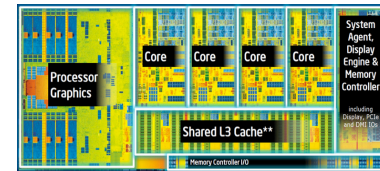
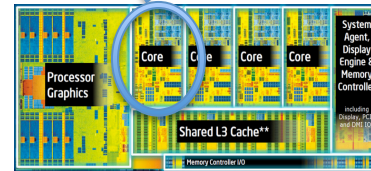
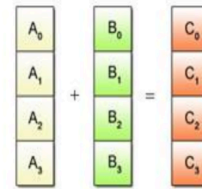




# Server multiprocessore

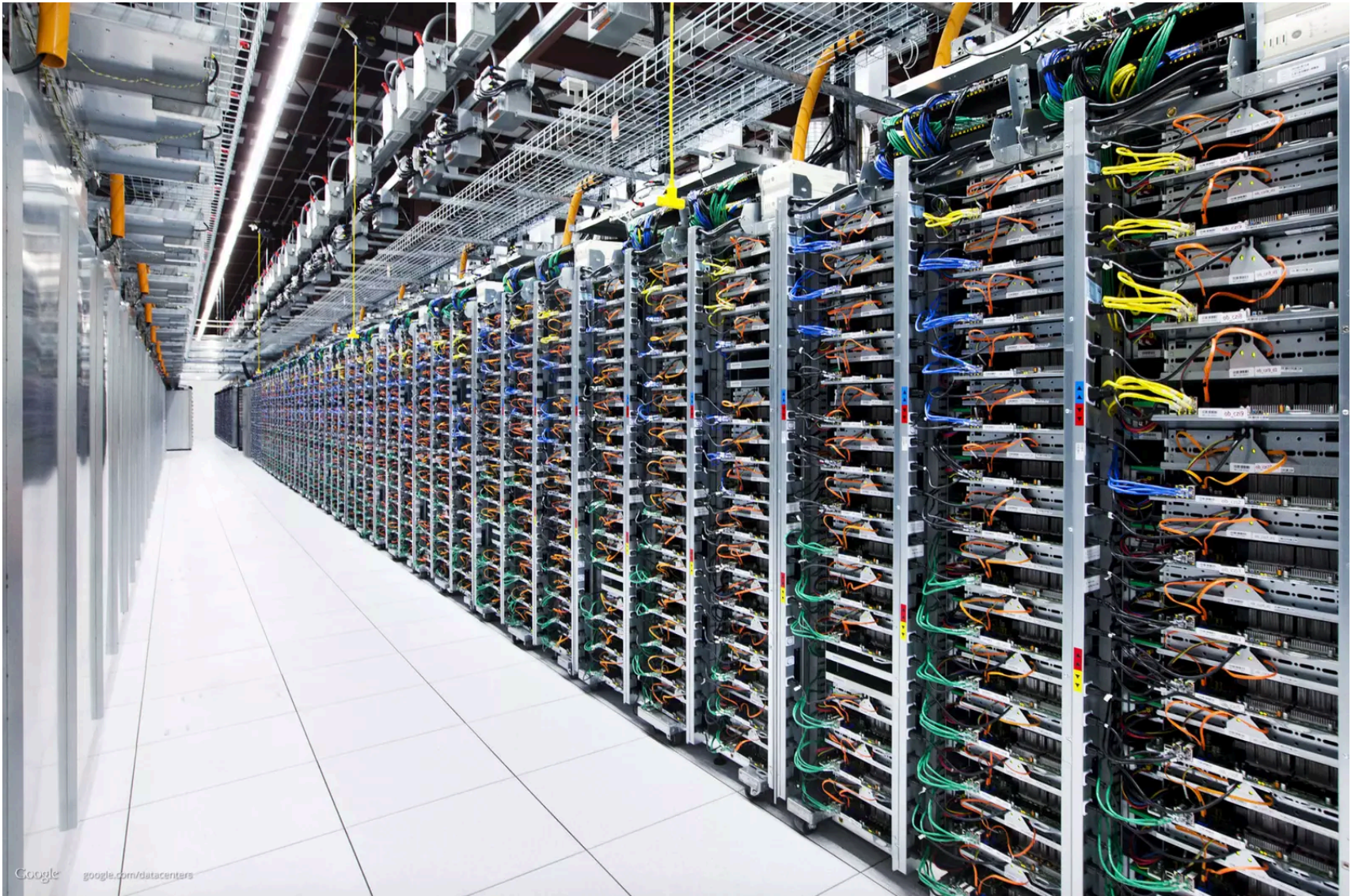


# Server rack (cluster)

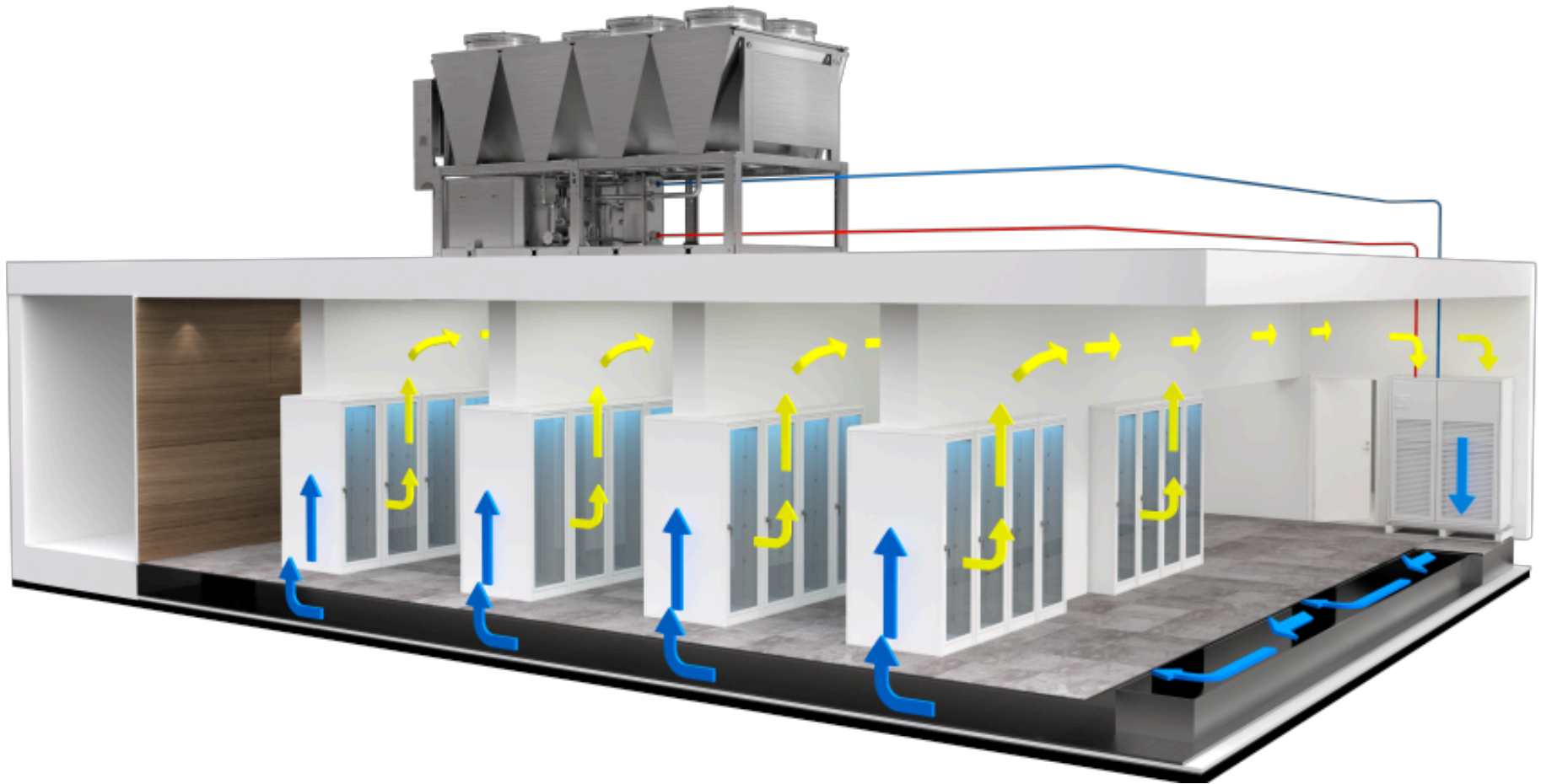




# Data center



# Data center





# Data center

